

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
Ming-Dou Ker et al.

Application No.: 09/944,171

Confirmation No.: 9842

Filed: September 4, 2001

Art Unit: 2815

For: ESD PROTECTION CIRCUIT WITH VERY  
LOW INPUT CAPACITANCE FOR HIGH-  
FREQUENCY I/O PORTS

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Examiner: Jackson Jr., Jerome

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INTRODUCTORY COMMENTS**

In response to the Examiner's Office Action dated September 28, 2007, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

The present Amendment includes: **amendments to the Claims; and**

**Remarks.**